

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	09/682,721	MATHEW ET AL.
	Examiner	Art Unit

Aaron C Perez-Daple

2154

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to RCE filed 6/25/04.
2.  The allowed claim(s) is/are 1-11, 13 and 16-20.
3.  The drawings filed on 10 October 2001 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

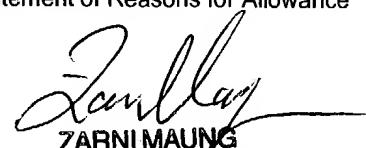
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

  
ZARNI MAUNG  
PRIMARY EXAMINER

**DETAILED ACTION**

1. This Action is in response to RCE filed 6/25/04, which has been fully considered.
2. Claims 1-20 are presented for examination.

**EXAMINER'S AMENDMENT**

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Jeffrey Chap on 7/20/04.

Claim 1 (currently amended):

A method for implementing a pre-designed state model, said method comprising:  
extracting state information from the state model via at least one [controller] state processor;  
processing said extracted state information via said at least one [controller] state processor;  
generating a state code and a state table in response to said processed extracted state information via said at least one [controller] state processor;  
compiling said state code to generate a runtime code via said at least one [controller] state processor;

transferring said runtime code and said state table to a designated controller that is separate from said at least one state processor; and  
implementing the state model by running said runtime code while utilizing information within said state table using [a] said designated controller [that is separate from said at least one controller].

Claim 11 (currently amended):

A state processor for generating a state table and a runtime code for use in implementing one or more pre-designed state models, said device comprising:  
a state model information provider extracting state model information in response to the one or more state models having hierarchical organized states;  
a state information separator generating a state code and the state table in response to the one or more state models; [and]  
an event organizer generating an event symbols header having global and shared event symbol definitions from an event configuration file in response to a header file, the event configuration file and said events symbol header defining events to be shared; and  
a compiler compiling said state code [and generating the runtime code] using said event symbols header to generate the runtime code.

Claim 12 (cancelled)

Claim 13 (currently amended):

In line 1, delete “claim 12” and insert --claim 11--.

Claim 14 (cancelled)

Claim 15 (cancelled)

***Allowable Subject Matter***

4. **Claims 1-11, 13 and 16-20** as amended above are allowed.
5. The following is an examiner's statement of reasons for allowance: As for claim 1, the prior art does not teach or suggest implementing a state model by running a runtime code while utilizing information within a state table using a designated controller that is separate from at least one state processor, wherein the state table and runtime code are generated by the at least one state processor. As for claims 8 and 11, the prior art does not teach or suggest generating an events symbols header having global and shared event symbol definitions, the event configuration file and said events symbol header defining events to be shared, and compiling one or more state codes using said events symbols header to generate one or more runtime codes. Therefore, the invention of claims 1-20 provides an improved method for implementing pre-designed state models.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

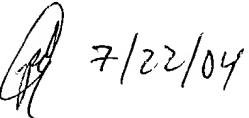
***Conclusion***

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aaron C Perez-Daple whose telephone number is (703) 305-4897. The examiner can normally be reached on 9am-5pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Follansbee can be reached on (703) 305-8498. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

 7/22/04  
Aaron Perez-Daple

  
ZARNI MAUNG  
PRIMARY EXAMINER

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